Jialin Lu 陆家林

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Education

Simon Fraser University

2018-2021 M.Sc. in Computer Science, supervised by Martin Ester

Zhejiang University

2014-2018 B.Eng. in Computer Science

2015-2018 B.Eng. in Industrial Design

Work Experience

2021 - now **Senior Engineer, Huawei Vancouver Research Center**

Performance engineer specializing in system-level programming and optimization across user-space networking, XDP/BPF, TLS, malware detection, and AI WAF. Contributions integrated and deployed in the USG series enterprise gateway and firewall products. Some selected corporate awards are

Outstanding Individual, 2025, for a major performance breakthrough (2 out of 150+ R&D)

Campaign Project Hero, 2025, for a major cross-department optimization campaign

Gold Medal Team, 2024, (1 out of 10+ teams)

Future Star, 2024

Research

See Google Scholar . I also write blogs on my homepage

Interpretable Drug Response Prediction using a Knowledge-Based Neural Network — KDD 2021, 2021
Oliver Snow, Hossein Sharifi Noghabi, Jialin Lu, et al.

Neural Disjunctive Normal Form — Master Thesis, 2020
Jialin Lu

Revisit Recurrent Attention Model from an Active Sampling Perspective — NeurIPS Neuro↔AI Workshop, 2019
Jialin Lu

An Active Approach for Model Interpretation — NeurIPS HCML Workshop, 2019
Jialin Lu, Martin Ester

Checking Functional Modularity in DNN By Biclustering Task-specific Hidden Neurons — NeurIPS Neuro↔AI Workshop, 2019
Jialin Lu, Martin Ester

Other Experience

Jan-Apr 2021 **Teaching Assistant** CMPT419/726 Machine Learning · Supervisor: Ke Li, Simon Fraser University

May-Aug 2020 **Teaching Assistant** CMPT353 Computational Data Science · Supervisor: Greg Baker, Simon Fraser University

2019-2021 **Research Assistant** Supervisor: Martin Ester, Simon Fraser University

Mar-Jun 2018 **Teaching Assistant** Artificial Intelligence · Supervisor: Xi Li, Zhejiang University

May-Aug 2018 **Internship** Fonda Smart Control, Hangzhou

Skills

Programming Languages: C (primary), C++ (proficient with most non-fancy features)

Rust: Occasional open-source contributions, including to the Typst typesetting engine (this doc is typesetted by Typst)

Performance Optimization: Workflow simplification, cache optimization, memory access patterns, zero-copy, scheduling, profiling tools